

| Ref # | Hits    | Search Query          | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|---------|-----------------------|---|------------------|---------|------------------|
| S1    | 65751   | phase near detect\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/06/20 09:34 |
| S2    | 15010   | loop near filter      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/21 16:47 |
| S3    | 108851  | error near signal     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/21 16:47 |
| S4    | 137951  | reference near signal | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/21 16:47 |
| S5    | 74515   | flip near flop        | USPAT   | OR               | OFF     | 2004/11/21 17:03 |
| S6    | 833333  | multiple latch        | USPAT   | OR               | OFF     | 2004/11/21 17:03 |
| S7    | 166755  | latch                 | USPAT   | OR               | OFF     | 2004/11/21 17:03 |
| S8    | 1790    | S1 and S5 and S7      | USPAT   | OR               | OFF     | 2004/11/21 17:03 |
| S9    | 432     | S8 and S2             | USPAT   | OR               | OFF     | 2004/11/21 16:48 |
| S10   | 95      | S9 and S3 and S4      | USPAT   | OR               | OFF     | 2004/11/21 17:04 |
| S11   | 10924   | "xor"                 | USPAT   | OR               | OFF     | 2004/11/21 17:04 |
| S12   | 16      | S10 and S11           | USPAT   | OR               | OFF     | 2004/11/21 17:04 |
| S16   | 142119  | flip near flop        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/21 17:03 |
| S17   | 1906338 | multiple latch        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/21 17:03 |

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|-----|--------|---|---|----|-----|------------------|
| S18 | 427941 | latch   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:03 |
| S19 | 1790   | S1 and S5 and S7  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:03 |
| S20 | 95     | S9 and S3 and S4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:04 |
| S21 | 95     | S9 and S3 and S4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:04 |
| S22 | 20564  | "xor"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:04 |
| S23 | 16     | S10 and S11   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/21 17:04 |
| S24 | 9      | ((("4222009") or ("4227251") or ("4305045") or ("4371974") or ("4380815") or ("4400667") or ("4400817"))).FN. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:18 |
| S25 | 1      | S24 and latch   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:19 |
| S26 | 467    | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3 or comparat\$3)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:20 |
| S27 | 292    | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3)                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:21 |

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| S28 | 6   | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3) and first adj signal with stor\$3 and second adj signal with first adj signal        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:24 |
| S29 | 6   | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3) and first adj signal with stor\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:24 |
| S30 | 44  | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3) and first adj signal and second adj signal   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 16:24 |
| S31 | 10  | ((phase adj lock\$3 adj loop) or (PLL)) and latch with phase adj (detect\$3) and first adj signal and second adj signal and third adj signal and error adj signal | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:28 |
| S32 | 428 | ((phase adj lock\$3 adj loop) or (PLL)) and latch and flip adj1 flop and first adj signal and second adj signal   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:29 |
| S33 | 215 | ((phase adj lock\$3 adj loop) or (PLL)) and latch same flip adj1 flop and first adj signal and second adj signal  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:29 |
| S34 | 26  | ((phase adj lock\$3 adj loop) or (PLL)) and latch same flip adj1 flop same phase adj (detect\$3 or comparat\$3) and first adj signal and second adj signal        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:34 |
| S35 | 0   | ((phase adj lock\$3 adj loop) or (PLL)) and latch with low with high same flip adj1 flop same phase adj (detect\$3 or comparat\$3)                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:34 |
| S36 | 9   | ((phase adj lock\$3 adj loop) or (PLL)) and latch with low with high same flip adj1 flop same phase adj (detect\$3 or comparat\$3)                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/17 18:34 |
| S37 | 115 | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (flip adj1 flop) with (low or fall) with (rise or high)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 09:35 |

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| S38 | 0  | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (flip adj1 flop) with (low or fall) with (rise or high) with phase adj (detector)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 09:35 |
| S39 | 3  | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (flip adj1 flop) with (low or fall) with (rise or high) same phase adj (detector)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 09:40 |
| S40 | 32 | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (flip adj1 flop) with (low or fall) with (rise or high) and phase adj (detect\$3)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 09:46 |
| S41 | 17 | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (low or fall) with (rise or high) and (flip adj1 flop) with (edge adj1 trigger\$2) and phase adj (detect\$3)                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 10:16 |
| S42 | 0  | ("29762.2").FN.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/20 09:52 |
| S43 | 2  | ("0029762").FN.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/20 09:52 |
| S44 | 5  | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (low or fall) with (rise or high) and (flip adj1 flop) with (negative adj1 edge adj1 trigger\$2) and phase adj (detect\$3)              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 10:29 |
| S45 | 6  | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (low or fall) with (rise or high) and (flip adj1 flop) with ((negative or fall\$3) adj1 edge adj1 trigger\$2) and phase adj (detect\$3) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 11:29 |
| S46 | 1  | ("6151356").FN.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/20 11:24 |
| S47 | 0  | S46 and negative adj1 edge   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JFO                         | OR | OFF | 2005/06/20 11:25 |

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| S48 | 1  | S46 and fall\$3 adj1 edge   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 11:25 |
| S49 | 10 | ((phase adj lock\$2 adj loop) or (PLL)) and latch with (low or fall) with (rise or high) and (flip adj flop) with ((negative or fall\$3) adj1 edge adj1 trigger\$2) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 11:30 |
| S50 | 6  | (phase adj detect\$3) and latch with (low or fall) with (rise or high) and (flip adj flop) with ((negative or fall\$3) adj1 edge adj1 trigger\$2)                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:20 |
| S51 | 1  | S46 and loop adj filter   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:20 |
| S52 | 0  | S46 and loop adj filter with phase with reference adj signal  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:21 |
| S53 | 0  | S46 and loop adj filter with reference adj signal   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:24 |
| S54 | 1  | S46 and reference adj signal and phase adj signal   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:28 |
| S55 | 25 | ((phase adj lock\$3 adj loop) or (PLL)) and (phase adj signal and reference adj signal) with (loop adj filter)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:37 |
| S56 | 17 | ((phase adj lock\$3 adj loop) or (PLL)) and (phase adj signal with reference adj signal) with (loop adj filter)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:39 |

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| S57 | 147 | ((phase adj lock\$3 adj loop) or (PLL)) and delay with (flip adj flop) with equal   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/06/20 12:54 |
| S58 | 9   | ((("4222009") or ("4227251") or ("4305045") or ("4371974") or ("4380815") or ("4400667") or ("4400817"))).PM.                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 12:58 |
| S59 | 2   | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) with (flip adj1 flop) with (inductor\$3)                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:30 |
| S60 | 649 | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:30 |
| S61 | 781 | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:30 |
| S62 | 0   | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj port with clock adj port  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:31 |
| S63 | 65  | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj input                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:32 |
| S64 | 0   | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj input same clock adj port | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:31 |
| S65 | 52  | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj input with clock          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 13:32 |
| S66 | 65  | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj input same clock          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 14:27 |
| S67 | 55  | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) and differential adj signal\$3 with data adj input same clock          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 14:26 |
| S68 | 3   | ((phase adj lock\$3 adj loop\$3) or (PLL)) and phase adj (detect\$3) same differential adj signal\$3 with data adj input                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO                         | OR | OFF | 2005/06/20 14:27 |

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| S69 | 18 | light adj4 diode and transmitt\$4 and photo adj1 diode and amplifier with receiver and phase adj detect\$3     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR | OFF | 2005/06/20 15:00 |
| S70 | 0  | light adj4 diode same transmitt\$4 same photo adj1 diode same amplifier with receiver same phase adj detect\$3 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR | OFF | 2005/06/20 15:00 |